

ABSTRACT OF THE DISCLOSURE

The invention provides a semiconductor device by which the size of an inverting amplifier ~~being~~ that is capable of intermittently outputting an oscillation signal can be downsized. The semiconductor device can be installed in parallel with an oscillator and include an inverting amplifier intermittently outputting oscillation signal in response to control signal. The inverting amplifier can include a transmission gate set to an “on” state where a first signal is transmitted in a case of the control signal being set to an H level, and set to an “off” state where a first signal is not transmitted in a case of the control signal being set to an L level, ~~level~~. The inverting amplifier can also include an inverter inverting a logical level of a signal given so as to output a second signal, and a clamping circuit set to make the first signal applied to an input terminal of the inverter in a case of the control signal being set to the level, and set to make predetermined voltage applied to the input terminal of the inverter when the control signal is set to a second logical level.